

LIST OF ARTS CITED BY APPLICANT
(Use separate sheet if necessary)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
MI22-1171SERIAL NO.
09/292,132APPLICANT
Salman Akram et al.FILING DATE
April 14, 1999GROUP
2812

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

Self	AR		Wolf, Ph.D., Stanley, "Silicon Processing for the VLSI Era - Volume 2: Process Integration," ©1990 Lattice Press, pages 212-213.
	AS		
	AT		

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1171		SERIAL NO. 09/272,122		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Suhman Abram et al.		FILING DATE April 14, 1999		
				GROUP 1812				
U.S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
Sum	AA	5,554,871	09/10/96	Yamashita et al.				
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		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
Sum	AK	JP 0252117	09/22/97	Japan			Abs.	
	AL	JP 0074199	03/16/97	Japan			Abs.	
	AM	JP 0215605	06/09/96	Japan			Abs.	
	AN	JP 8181309	07/12/96	Japan			Abs.	
	AO	JP 8139315	05/31/96	Japan			Abs.	
	AP	JP 8051108	02/20/96	Japan			Abs.	
	AQ	JP 5251463	09/28/93	Japan			Abs.	
Sum	AR	JP 4063974	02/27/93	Japan			Abs.	
OTHER REFERENCES (including Author, Title, Date, Portion, Pages, Etc.)								
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	AT							
	AU							
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Sum				9/21/04				
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